

### Remarks

Applicant's second Request for Continued Examination submitted concurrently with this amendment is to make of record art that has been brought to applicant's attention, to correct several previously submitted claims and to add a number of additional claims that emphasize further distinguishing features of the invention. The examiner's previous allowance of the application is noted with appreciation.

Applicant requests that the Japanese published Utility Model application No. 1-100471 ("the published Jp. Application") be reviewed and made of record in relation to this application. The published Jp. Application was pointed out to applicant by a third party, prospective licensee of the applicant's U.S. patent Number 5,990,776, the parent of the present application. The published Jp. Application and a translation into English is enclosed, and the published Jp. application is identified on the accompanying form PTO-1449. The published Jp. Application is believed not to adversely affect the patentability of the claims in this application. No representation as to the materiality of the published Jp. Application respecting this application is made.

The examiner in charge of this application is also requested to review and make of record the art cited by the examiner and the applicant in the application prior to applicant's filing of the Request for Continuing Examination herein and the art identified previously on and submitted with the previous Supplemental Disclosure Statement filed at the time of filing the previous Request for Continuing Examination. A checklist of previously cited art is appended for the examiner's convenience.

Amendments have been made to claims 19, 23 and 33 to correct obvious typographical or grammatical errors.

New claims 44 to 57 have been added. These differ from the art of record in (a) claiming a "power processing device," whereas certain of the art, like the published Jp. Application are not related to a power processing device, and (b) by setting forth more particularly that the one-or-more magnetic element cores form a closed magnetic path. This again distinguishes the claimed invention from art of record including the published Jp. Application.

The configuration of the cores as claimed in the new claims 44 to 57 are as described at page 11, lines 19 to 21 of the specification and as illustrated in the drawings at Figs. 1, 2, 3A and 3B. The thermal path of claims 48 and 49 is described at page 11, line 15 to 18. The direct connection of printed windings of claim 56 and the electrical connection of power components through conductive vias of claim 57 are described at page 11, lines 22 to page 12, line 10.

With the foregoing and the accompanying Information Disclosure Statement and art, it is respectfully urged that this application continues to be in condition for allowance and favorable reconsideration to that end is respectfully requested.

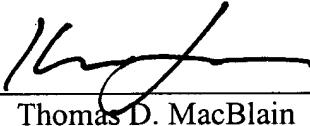
In addition to the enclosed check for \$1,126.00, authorization is given to charge any additional fees associated with this communication to Deposit Account No. 070135.

Should the examiner in charge of this application have questions or suggestions for the undersigned attorneys for applicants, he is invited to call or email the undersigned at the telephone number or email address given below.

Respectfully submitted,

**GALLAGHER & KENNEDY, P.A.**

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Information of: Ionel Jitaru :  
Serial No.: 09/434,985 : Examiner: Mai, Anh T.  
Filed: 11/5/1999 : Group Art Unit: 2832

Title: **Low Noise Full Integrated Multilayer Magnetic for Power  
Converters**

**CHECKLIST OF PREVIOUSLY CITED ART**

<b>U.S. PATENT NO.</b>	<b>ISSUE DATE</b>	<b>NAME OF PATENTEE</b>
4,622,627	11/11/1986	Rodriguez et al.
4,730,241	3/8/1988	Takaya
4,873,757	10/17/1989	Williams
5,126,714	6/30/1992	Johnson
5,184,103	2/2/1998	Gadreau et al.
5,312,674	5/17/1994	Haertling et al.
5,321,380	6/14/1994	Godek et al
5,353,001	10/4/1994	Meinel et al.
5,532,667	7/2/1996	Haertling et al.
5,754,088	5/19/1998	Fletcher et al.
5,929,733	7/27/1999	Anzawa
5,949,191	9/7/1999	Cassese et al.
6,069,548	5/30/2000	Baarman et al.
6,073,339	6/13/2000	Levin

**ARTICLES**

Bijan E. Mohandes "Designing High Frequency PWM Converters Using Integrated PCB Transformers" HFPC - April 1994 Proceedings, pages 551-560.

H. Martin et al. "Improved Multi-Phase Coupled Inductor Topologies," HFPC - May 1992 Proceedings, pages 340-349.